

In re the Patent Application of:  
E. Kelly et al  
Division of Serial No. 08/702,771  
Filed: 08/22/96  
For: TRANSLATED MEMORY PROTECTION  
APPARATUS FOR AN ADVANCED MICROPROCESSOR)  
Examiner:  
T. Thai  
Art Unit 2752

Preliminary Amendment

The Asst. Commissioner for Patents  
Washington, D. C. 20231

Dear Sir:

Please amend the above-identified divisional patent application as follows:

In the specification:

Page 1, line 4, after "BACKGROUND OF THE INVENTION", insert

--This application is a division of U. S. patent application 08/702,771,  
filed August 22, 1996, now U. S. Patent 6199,152.

In the claims:

(Pm)

- 1 Claim 1 (amended). A system for [protecting memory from being written] maintaining translation consistency in a computer which
- 2 includes a host processor designed to execute instructions of a host
- 3 instruction set and software for translating instructions from a target
- 4 instruction set to instructions of the host instruction set comprising:
- 5

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